



Jorge Martínez

Education

- 2007–2015 **Electronic Engineer**, *Universidad Tecnológica de Pereira*, Pereira.
2016–2018 **Master's degree in Electrical Engineering**, *Universidad Tecnológica de Pereira*, Pereira.

Master thesis

- Title Automatic synthesis of control strategies through temporal logics and Petri Nets for motion planning in autonomous systems
Supervisors Ph.D Mauricio Holguín Londoño

Experience

Vocational

- 2016–2018 **Teacher**, *Universidad Tecnológica de Pereira*, Pereira.
Hardware Design FPGA's, Industrial Automation, Introduction to Electronic Engineering, Electrical Maintenance.
2017–2018 **Researcher**, *Universidad Tecnológica de Pereira*, Pereira.
Project: A Methodology to continuous synthesize finite states automatons through formal languages and temporal logics in Autonomous Ground Navigation Applications.

Miscellaneous

- 2013–2013 **Technical in Metrology of Electrical Variables**, *Metrology Laboratory of Electrical Variables - UTP*, Pereira.
Electrical Equipment Calibration

Languages

- Spanish 100% *Native*
English Intermediate proficiency

street 8 number 4-211 – 661020 – Santa Rosa de Cabal - Colombia

📞 +57 3217335404 • ✉️ jolumartinez@utp.edu.co • 🌐 jolumartinez.com

Computer skills

Programming and markup Languages Python, Matlab, LabVIEW, VHDL, C, C++, PHP, SQL, LaTeX.

Interests

Ride Bicycle Exercise helps me keep fit.
Play Piano It relaxes me and allows me to develop other skills.
Travel I like to travel and to know other cultures
Literature I enjoy reading classic literature

Other Skills

- Knowledge of research methodologies
- Computer vision
- Printed Circuit Design

References

Ph.D Mauricio Holguín (MsC Advisor)	Ph.D Andrés Escobar Mejía	Ph.D(C) Germán Andrés Holguín
Universidad Tecnológica de Pereira	Universidad Tecnológica de Pereira	Universidad Tecnológica de Pereira

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